

THTA6482

Octal 32GBaud PAM4/NRZ TIA

General Description

The THTA6482 is a high performance linear Octal TIA for optical links with data rates up to 32GBaud PAM4/NRZ.

Features include RSSI for photo-alignment and power monitoring, and I2C control of bandwidth, output amplitude, peaking, LOS, gain and other parameters.

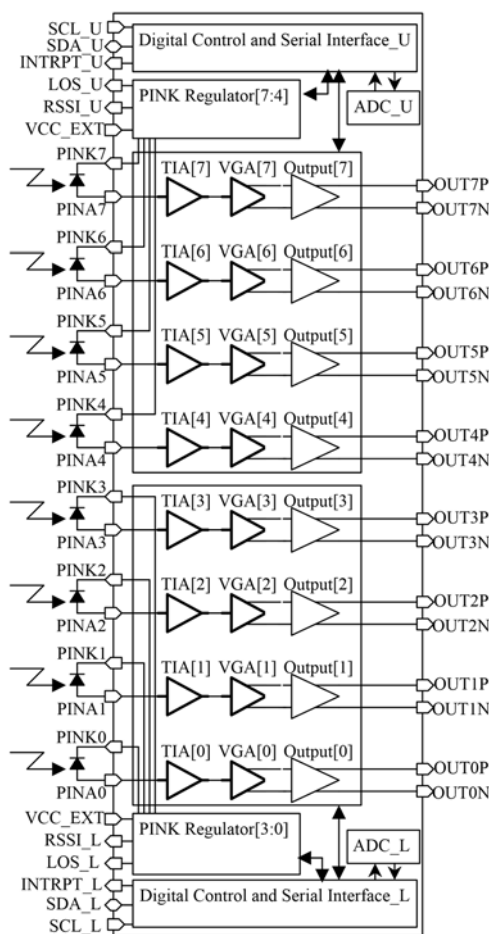
Applications

PCI Express 6.0 Optical Modules for MMF links

Features

- Octal 32GBaud PAM4/NRZ TIA
- Ultra-low Power: 210mW/Ch (@3.3V)
- 250µm anode to anode spacing
- Wide Bandwidth with Low Noise
- Integrated AGC loop
- RSSI for photo-alignment and monitoring
- Flexible, integrated PINK regulator enabling very large and small reverse bias voltage on PD.
- Support squelch function
- Support PCI Express 6.0 Low Power States
- Supports 0 °C to +95 °C die temperature

Block Diagram



Chip Dimensions

Die Thickness: 108 μm $\pm$ 12.5 μm , Pad Material: Aluminum. Data Base Size: 3800 x 3600.

Table 1 Die Detail. All Dimensions in μm .

Part Number	Minimum Die Size	Maximum Die Size	Pad Openings	Pad Openings For PINA & OUTP/N	Pad opening for PINK pads
THTA6482	3870 x 3670	3920 x 3720	70.2 x 70.2 square	70.2 x 70.2 octagon	81 x 60 rectangular

Absolute Maximum Ratings**Table 2 Absolute Maximum Ratings**

Parameter		Min	Typ	Max	Unit
PINA Input Current (Average)		-	-	4	mA
ESD Rating	HBM at high-speed pins	± 500	-	-	V
	HBM at all other pins	± 2000	-	-	V

Recommended Operating Conditions**Table 3 Recommended Operating Conditions**

Parameter	Min	Typ	Max	Unit
Core Power Supply Voltage (VCC25)	2.4	2.5	2.6	V
TIA Power Supply Voltage (VCC33)	3.1	3.3	3.6	V

Performance Specification**Table 4 TIA Input DC / AC specifications**

Over recommended operating supply and temperature range unless otherwise specified

Symbol	Parameter	Condition	Min	Typ	Max	Unit
ICCW33	3.3V Active Supply Current	Normal state	-	34	-	mA
ICCW25	2.5V Active Supply Current	Normal state	-	37	-	mA
ISENSPAM	Input Sensitivity	BER < 10^{-6} at 32Gbaud PAM4	-	130	-	μApp
IEIN	Equivalent Input Noise	At 6.2 k Ω differential transimpedance gain	-	3.0	-	μARMS
BRATE	PAM4/NRZ Input Data Rate		1	-	32	GBaud
SS TZMAX	Maximum Differential Transimpedance	-	-	6.3	-	k Ω
SS TZMIN	Minimum Differential Transimpedance	-	-	0.45	-	k Ω
IOL	Overload Input Current	-	-	1.8	-	mApp
TAMP_LOS_SLOW	Assert/De-assert Time for an AMP LOS Event from any channel in Slow LOS mode	-	-	-	1	μs
R TX-DIFF-DC	DC Differential Impedance	-	80	-	120	Ω
V TX-DIFF-PP-SWING	Target Output Amplitude Range	-	-	575 to 950	-	mVpp

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