



News Release

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THine and Altera enter partnership to support V-by-One® HS Interface

Tokyo (July 7, 2009) – THine Electronics, Inc. (JASDAQ:6769), the global leader in high-speed serial interface and provider of mixed-signal LSI for flat panel displays, announced today that it is turning to FPGAs to support its high-speed serial interface, V-by-One HS. Altera Corporation has partnered with THine to offer a V-by-One IP reference design on Altera's FPGAs which include family support from Altera's Arria® and Stratix® series devices.

Developed for the television display market, THine's V-by-One® HS offers higher resolution and clearer picture quality while lowering the total cost of internal cables and connectors. The advanced high-speed interface is essential for applications with high-speed data transmission such as next generation flat panel displays and multi-functional printers.

“The demand for high definition and better quality images has prompted television set makers and display panel vendors to look at advanced interface technology to meet the growing need,” said Mr. John Sakamoto, senior director of application business units at Altera. “Adding V-by-One® HS capability in our FPGA addresses this need and enables our customers to begin V-by-One® development today.”

“Since 2008, THine's V-by-One® HS technical specification has been widely accepted by the global markets,” said Mr. Masahiro Kato, senior vice president, THine Electronics, Inc. “Television makers and panel vendors will benefit from our partnership with Altera by accelerating their time to market and differentiating their designs with Altera’s FPGAs and THine's V-by-One® HS.”

■ Key benefits of V-by-One® HS

- High transmission quality with high performance equalizer in noisy conditions
- High data transmission quality solving cable skew problems with high speed Serializer/Deserializer using Clock Data Recovery technology
- Lower EMI with clock embedded transmission, no reference clock at receiver
- Reduction of total cost and board space by optimizing cables and connectors
- Seamless transition to V-by-One® HS minimizing change of device input/output and peripheral design
- Lower energy consumption with variable transmission speed: 600 Mbps to 3.75 Gbps
- Ease of use, Plug & Play

— Open standard disclosed by THine

■ Availability

The V-by-One IP reference design is available today through Altera's partner, Bitec.

About THine Electronics, Inc.

THine Electronics Incorporated is a fables maker that provides innovative mixed signal LSI and analog technologies such as LVDS, other high speed data signaling, timing controller, Analog-to-Digital Converter (ADC), Image Signal Processor (ISP), RF and power management in growing niche markets for our customers' solutions, targeting its strategic markets in flat TVs, LCD monitors, projectors, projection televisions, mobile phones, and automotive markets.

THine is headquartered in Tokyo, Japan, and has design centers in Kyoto and Kyushu, Japan, as well as subsidiaries in Tokyo and Taipei, Taiwan. THine Electronics is traded on the JASDAQ under the security code of 6769. More information is located on the World Wide Web at www.thine.co.jp.

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